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Attorney Docket: 8728-426 (JP9-2000-0187US1)  
PATENT

1775

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Andry et al.

Examiner: Lam, Cathy Fong Fong

Serial No: 09/971,953

Group Art Unit: 1775

Filed: October 5, 2001

Docket: 8728-426 (JP9-2000-0187US1)

For: **METHOD FOR FORMING PATTERNS FOR SEMICONDUCTOR DEVICES**

Director of the U.S. Patent and Trademark Office  
Washington, D.C. 20231

**RESPONSE TO RESTRICTION REQUIREMENT**

Sir:

In response to the office action dated August 20, 2002 applicant provisionally elects claims 1-17, with traverse. Claims 1-21 are pending in the current application. While the subject matters disclose in claims 1-21 may be distinct for reasons set forth by the Examiner, Applicant believes that simultaneous examination of all the claims will not present an undue burden on the Examiner. The claims include subject matters that are commonly related to a semiconductor device. Under such circumstances, the Examiner is encouraged to maintain all claims in the same application. See MPEP § 803.

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**CERTIFICATE OF MAILING 37 C.F.R. § 1.8(a)**


I hereby certify that this correspondence (and any document referred to as being attached or enclosed) is being deposited with the United States Postal Service as first class mail, postage paid in an addressed to: Director of the U.S. Patent and Trademark Office, Washington, D.C. 20231 on September 20, 2002.

Dated: 9/20/02

  
Frank Chau

Early and favorable consideration of this application is earnestly solicited.

Respectfully Submitted

By:   
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